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DWG NO.  
130-0321321-A1

REV  
A1

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NOTES: UNLESS OTHERWISE SPECIFIED

1. ASSEMBLY HOUSE TO VISUALLY VERIFY BOARD BEING ASSEMBLED HAS INTEL P/N 100-0321321-A1 IN ETCH ON BOTTOM SIDE.

2. BOARDS MUST BE BAKED FOR A DURATION OF 8 HOURS AT 250 DEGREES FARENHEIT IMMEDIATELY PRIOR TO ASSEMBLY.

3. ASSEMBLE BOARD PER IPC-A-610(LATEST REVISION) STANDARD.

4. ALL MATERIALS USED MUST BE RoHS COMPLIANT.

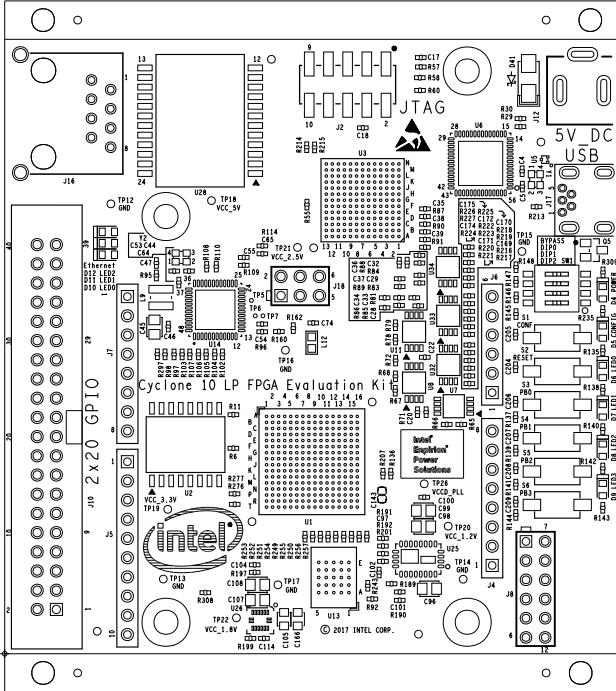
5. HANDLE MOISTURE SENSITIVE DEVICES(MSD) PER MANUFACTURE'S CALL OUT ON MSD LABEL. IF MSD LABEL IS MISSING OR NOT LEGIBLE THEN ALL PLASTIC PACKAGED COMPONENTS ARE TO BE CONSIDERED MSD LEVEL 6 AND USE EIA/JEP 113-A FOR HANDLING.

6. REWORK MUST BE APPROVED BY CUSTOMER BEFORE BEING ACCEPTIBLE USE IPC-R-700(LATEST REVISION) FOR ALL PCA REWORK APPROVED.


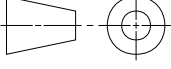
7. SQUARE PIN PAD DENOTE PIN 1 OF ICs/CONNECTORS, POSITIVE END FOR CAPACITORS OR CATHODE END FOR DIODES.

8. INSTALL BAR CODE LABEL APPROX WHERE SHOWN, INCLUDING S/N.

9. VENDOR TO KEEP QUALITY CONTROL INK STAMPINGS OFF SIGNAL TRACES AND PADS EVEN IF COVERED BY SOLDERMASK.



PRIMARY SIDE SHOWN

			8	DWN PSG Board Design Team 14-MAY-2017	 (c) 2017 INTEL Corporation 101 INNOVATION DRIVE SAN JOSE, CA 95134	
			SPEC	3		CHKR
XX-XXX-XXXX						DSGN ENGR PSG Board Design Team
DASH	NEXT ASSY	USED ON	FNSH	19 21 23		RELEASE
APPLICATION						
PROJECTION		UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCHES IN ACCORDANCE WITH ANSI Y14.5. DRILL TOLERANCES ARE PER 10387			TOLERANCES: DEC .X ± .01 DEC .XX ± .01 DEC .XXX ± .005 DEC .XXXX ± .002 FRACTIONS ± .0 ANGLES ± .1°	
					CONTRACT NO. XXXXXXXXXX	
SIZE D	CAGE CODE N/A	DWG NO. 130-0321321-A1		REV A1		
SCALE: 1/1				SHEET 1 OF 1		