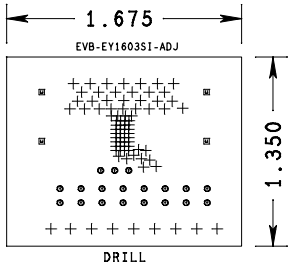
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

PHYSICAL BOARD DIMENSIONS & LAYER STRUCTURE

DRILL CHART: TOP to BOTTOM				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
+	12.0	+3.0/-3.0	PLATED	83
⊕	40.1575	+3.0/-3.0	PLATED	19
⊠	100.0	+3.0/-3.0	PLATED	4



NOTES:
1. THIS BOARD IS RoHS COMPLIANT.
2. PRINTED WIRING BOARD DESIGN AND ACCEPTANCE CRITERIA SHALL BE IAW WITH THE REQUIREMENTS OF IPC-D-275 AND IPC-A-600.
3. MATERIAL: FR4 , 1 OZ COPPER.
4. APPLY SOLDER MASK, BOTH SIDES OVER BARE COPPER IAW IPC-SM-840. CLASS 2 (LPI) (GREEN MASK).
5. ALL PATTERNS ARE VIEWED FROM THE PRIMARY SIDE LOOKING THROUGH THE BOARD.
6. UNLESS OTHERWISE SPECIFIED ALL HOLE DIAMETERS ARE AFTER PLATING.
7. APPLY SILKSCREEN USING WHITE NON-CONDUCTIVE EPOXY BASED INK.
8. PWB MUST BE 100% ELECTRICALLY TESTED FOR SHORTS AND CONTINUITY. USE NETLIST PROVIDED EVB-EY1603SI-ADJ IPC356.IPC IAW IPC-D-356.
9. MARK DATE CODE AND MANUFACTURES IDENIFICATION ON SOLDER SIDE PER IPC-6011 AND IPC-6012.
10. TOLERANCE ON ALL DRILL HOLES SHALL BE IAW IPC-D-2221 & 2222 UNLESS OTHERWISE SPECIFIED.

DASH NO	NEXT ASSY
APPLICATION	

PART TO BE FREE OF BURRS AND SHARP EDGES
COMMERCIAL TOLERANCES APPLY TO STOCK SIZES
MANUFACTURING TOLERANCES PER 000002
WORKMANSHIP PER 000000
SCREW THREADS PER ANSI Y14.6
ABBREVIATIONS PER MIL-STD-12
LOGIC SYMBOLS PER IEEE STD 91
DIMENSIONING AND TOLERANCING PER ANSI Y14.5
ELECTRICAL AND ELECTRONIC DIAGRAM PER ANSI Y14.15
GRAPHIC SYMBOLS FOR ELECTRICAL AND ELECTRONIC DIAGRAM PER IEEE STD 315
ELECTRICAL AND ELECTRONIC REFERENCE DESIGNATIONS PER IEEE STD 200
WELDING SYMBOLS PER ANSI/AWS A2.4
INTERPRET DRAWING IAW DOD-STD-100

UNLESS OTHERWISE SPECIFIED
DIMENSION ARE IN INCHES
AND INCLUDE APPLIED FINISH
TOLERANCES
2 PLACE DIN. ± .---
3 PLACE DIN. ± .---
ANGLES ± .--

CONTRACT NO.		
DSN BY	CHK BY	
ENGR	ELEC ENGR	
PROJ ENGR	MFG ENGR	
RELIABILITY	CONFIGURATION	
APPROVAL- DFTG	QA	
APPROVAL	SYS ENGR	

PRINTED WIRING BOARD, EVAL-
EVB-EY1603SI-ADJ

SIZE	DWG NO.	REV
D	09348	A
SCALE 2/1		SHEET 1 OF 1